

1. Rig up the circuit as shown in the Figure 8.1
3. Using AFO, apply 1 kHz sinusoidal input with a peak-to-peak amplitude of 1V. Observe the input and output using CRO.
4. Keeping input voltage constant, vary the input frequency from 10 Hz to 10 kHz and note down the amplitude change in output voltage waveform. Tabulate these results in dB and plot the frequency response.

B. Notch Filter

Circuit Diagram:

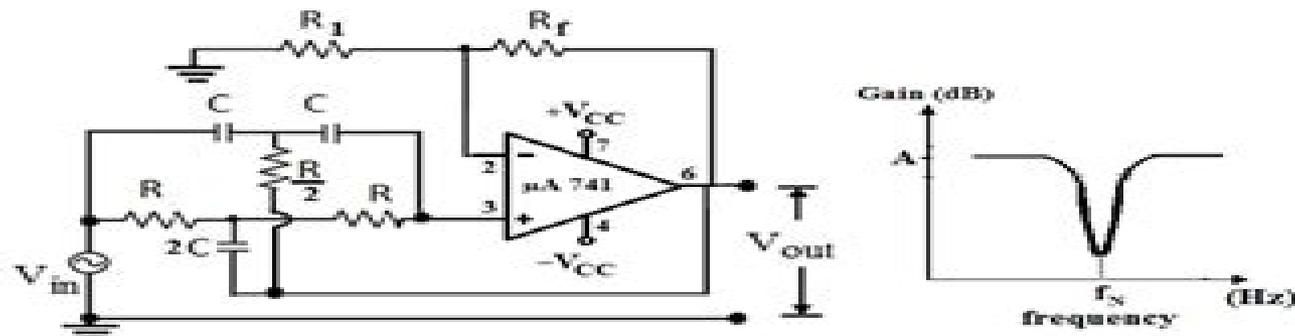


Figure 8.2: Notch filter and its frequency response

Design:

$f_c = 60\text{Hz}$, Pass band gain = 2

Let $C = 0.0047\mu\text{F}$. Then

$$R = \frac{Q}{\pi f_n C} = 56.44\text{k}\Omega$$

Let $R_1 = 10\text{k}\Omega$. Therefore, $A_p = 1 + \frac{R_f}{R_1} = 2$, then $R_2 = 10\text{k}\Omega$.

Procedure:

1. Rig up the circuit as shown in the Figure 8.2
2. Using AFO, apply 1 kHz sinusoidal input with a peak-to-peak amplitude of 1V. Observe the input and output using CRO.
3. Keeping input voltage constant, vary the input frequency from 10 Hz to 10 kHz and note down the amplitude change in output voltage waveform. Tabulate these results in dB and plot the frequency response

Electronic System Design Lab Manual

Ensheng Dong



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