Microcontroller Solutions

Chapter 5:-

Section 5.1 and 5,2:-

3. See on page 123 Figure 5-1, and on page 124 figure 5-2.
4. Register bank 1 , 2 , 3 share the space with stack because by default stack starts from 07H and after increment data is stored in 08H on the other hand Register bank 1 address starts from 08H as well see page 123 fig 5-1.
6. It copies the contents of the location 0F0H into the accumulator rather than the value 0F0H.
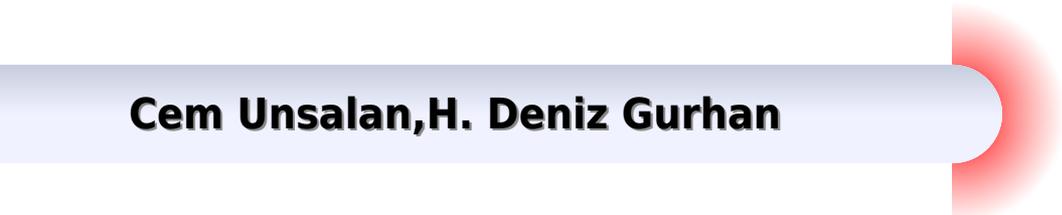7. Same as question nos 6.

8. ORG 0000H
   MOV R0,#50H
   MOV R1,#40H
   MOV R3,#30H
   PUSH 00H
   PUSH 01H
   PUSH 03H
   POP 1DH POP
   1EH
   POP 1FH
   END
9. Registers R0 and R1.

10. ORG 0000H
    MOV A,#0FFH
    MOV R7,#32
    MOV R0,#50H
    NAME:MOV @R0,A

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