

HSPICE® Reference Manual: Commands and Control Options

Version E-2018.10, December 2018

SYNOPSIS™

Hspice User Guide

Patrick Vollmar



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HSPICE User's Manual ,1996 **MOSFET Modeling & BSIM3 User's Guide** Yuhua Cheng,Chenming

Hu,2007-05-08 Circuit simulation is essential in integrated circuit design and the accuracy of circuit simulation depends on the accuracy of the transistor model BSIM3v3 BSIM for Berkeley Short channel IGFET Model has been selected as the first MOSFET model for standardization by the Compact Model Council a consortium of leading companies in semiconductor and design tools In the next few years many fabless and integrated semiconductor companies are expected to switch from dozens of other MOSFET models to BSIM3 This will require many device engineers and most circuit designers to learn the basics of BSIM3 MOSFET Modeling BSIM3 User s Guide explains the detailed physical effects that are important in modeling MOSFETs and presents the derivations of compact model expressions so that users can understand the physical meaning of the model equations and parameters It is the first book devoted to BSIM3 It treats the BSIM3 model in detail as used in digital analog and RF circuit design It covers the complete set of models i e I V model capacitance model noise model parasitics model substrate current model temperature effect model and non quasi static model MOSFET Modeling BSIM3 User s Guide not only addresses the device modeling issues but also provides a user s guide to the device or circuit design engineers who use the BSIM3 model in digital analog circuit design RF modeling statistical modeling and technology prediction This book is written for circuit designers and device engineers as well as device scientists worldwide It is also suitable as a reference for graduate courses and courses in circuit design or device modelling Furthermore it can be used as a textbook for industry courses devoted to BSIM3 MOSFET Modeling BSIM3 User s Guide is comprehensive and practical It is balanced between the background information and advanced discussion of BSIM3 It is helpful to experts and students alike

HSPICE User's Manual ,1996 HSPICE User's Manual ,1996 **HSPICE User's Manual** ,1992 *Compact*

Models for Integrated Circuit Design Samar K. Saha,2018-09-03 *Compact Models for Integrated Circuit Design Conventional Transistors and Beyond* provides a modern treatise on compact models for circuit computer aided design CAD Written by an author with more than 25 years of industry experience in semiconductor processes devices and circuit CAD and more than 10 years of academic experience in teaching compact modeling courses this first of its kind book on compact SPICE models for very large scale integrated VLSI chip design offers a balanced presentation of compact modeling crucial for addressing current modeling challenges and understanding new models for emerging devices Starting from basic semiconductor physics and covering state of the art device regimes from conventional micron to nanometer this text Presents industry standard models for bipolar junction transistors BJTs metal oxide semiconductor MOS field effect transistors FETs FinFETs and tunnel field effect transistors TFETs along with statistical MOS models Discusses the major issue of process variability which severely impacts device and circuit performance in advanced technologies and requires statistical compact models Promotes further research of the evolution and development of compact models for VLSI circuit design and analysis Supplies

fundamental and practical knowledge necessary for efficient integrated circuit IC design using nanoscale devices Includes exercise problems at the end of each chapter and extensive references at the end of the book Compact Models for Integrated Circuit Design Conventional Transistors and Beyond is intended for senior undergraduate and graduate courses in electrical and electronics engineering as well as for researchers and practitioners working in the area of electron devices However even those unfamiliar with semiconductor physics gain a solid grasp of compact modeling concepts from this book

CMOS Analog Circuit Design Phillip E. Allen, Douglas R. Holberg, 2012-07-19 This work presents an effective overview of the principles and techniques for designing circuits to be implemented in CMOS technology It explains the methodology of analogue integrated circuit design by using a hierarchically organised approach

Smart Power ICs Bruno Murari, 2002-06-13 This book provides a survey of the state of the art of technology and future trends in the new family of Smart Power ICs and describes design and applications in a variety of fields ranging from automotive to telecommunications reliability evaluation and qualification procedures The book is a valuable source of information and reference for both power IC design specialists and to all those concerned with applications the development of digital circuits and with system architecture

The SPICE Book Andrei Vladimirescu, 1994 This new book written by Andre Vladimirescu who was instrumental in the development of SPICE at the University of California Berkeley introduces computer simulation of electrical and electronics circuits based on the SPICE standard Relying on the functionality first supported in SPICE2 that is now supported in all SPICE programs this text is addressed to all users of electrical simulation The approach to learning circuit simulation is to interpret simulation results in relation to electrical engineering fundamentals the book asks the student to solve most circuit examples by hand before verifying the results with SPICE Addressed to both the SPICE novice and the experienced user the first six chapters provide the relevant information on SPICE functionality for the analysis of linear as well as nonlinear circuits Each of these chapters starts out with a linear example accessible to any new user of SPICE and proceeds with nonlinear transistor circuits The latter part of the book goes into more detail on such issues as functional and hierarchical models distortion analysis basic algorithms in SPICE and related options parameters and how to direct SPICE to find a solution when it does not converge to a solution The approach emphasizes that SPICE is not a substitute for knowledge of circuit operation but a complement The SPICE Book is different from previously published books in the approach of solving circuit problems with a computer The solution to most circuit examples is sketched out by hand first and followed by a SPICE verification For more complex circuits it is not feasible to find the solution by hand but the approach stresses the need for the SPICE user to understand the results Readers gain a better comprehension of SPICE thanks to the importance placed on the relation between EE fundamentals and computer simulation The tutorial approach advances from the hand solution of a circuit to SPICE verification and simulation results interpretation This book teaches the approach to electrical circuit simulation rather than a specific simulation program Examples are simulated alternatively with

SPICE2 SPICE3 or PSPICE Accurate descriptions simulation rationale and cogent explanations make this an invaluable reference

MOSFET Models for SPICE Simulation William Liu,2001-02-21 An expert guide to understanding and making optimum use of BSIM Used by more chip designers worldwide than any other comparable model the Berkeley Short Channel IGFET Model BSIM has over the past few years established itself as the de facto standard MOSFET SPICE model for circuit simulation and CMOS technology development Yet until now there have been no independent expert guides or tutorials to supplement the various BSIM manuals currently available Written by a noted expert in the field this book fills that gap in the literature by providing a comprehensive guide to understanding and making optimal use of BSIM3 and BSIM4 Drawing upon his extensive experience designing with BSIM William Liu provides a brief history of the model discusses the various advantages of BSIM over other models and explores the reasons why BSIM3 has been adopted by the majority of circuit manufacturers He then provides engineers with the detailed practical information and guidance they need to master all of BSIM s features He Summarizes key BSIM3 components Represents the BSIM3 model with equivalent circuits for various operating conditions Provides a comprehensive glossary of modeling terminology Lists alphabetically BSIM3 parameters along with their meanings and relevant equations Explores BSIM3 s flaws and provides improvement suggestions Describes all of BSIM4 s improvements and new features Provides useful SPICE files which are available online at the Wiley ftp site

Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology ,1997

A Methodology for Modeling the Manufacturability of Integrated Circuits Eric David Boskin,1995

MOSFET Modeling with SPICE Daniel Foty,1997 This book will help CMOS circuit designers make the best possible use of SPICE models and will prepare them for new models that may soon be introduced Introduces SPICE modeling and its use in CMOS circuit design Presents the formalism of model building and the semiconductor physics of MOS structures Covers each important SPICE model showing how to choose the appropriate model Discusses the popular HSPICE Level 28 as well as Levels 1 3 BSIM 1 3 and MOS Model 9 Presents techniques for accounting for systematic process variations Describes new model candidates including the Power Lane Model the PCIM Model and the EKV Model Includes extensive examples throughout Practicing engineers and scientists in the semiconductor industry engineering faculty and students

Statistical Models, Methods, and Algorithms for Computer-aided Design Manufacturing Michael Eugene Orshansky,2001

Implementation of an Active-R Opamp Circuit Using Semi-custom Technology Kenneth Brian Gee,1988

Semiconductor Device Modeling with Spice Giuseppe Massabrio,Paolo Antognetti,1998-12-22

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GMD Research Series ,1998

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