



# FIFO Partitioner Megafunction

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## User Guide



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# Megafunction User Guide

**G Orfield**



## **Megafunction User Guide:**

**Building Embedded Systems** Changyi Gu, 2016-05-26 Develop the software and hardware you never think about We re talking about the nitty gritty behind the buttons on your microwave inside your thermostat inside the keyboard used to type this description and even running the monitor on which you are reading it now Such stuff is termed embedded systems and this book shows how to design and develop embedded systems at a professional level Because yes many people quietly make a successful career doing just that Building embedded systems can be both fun and intimidating Putting together an embedded system requires skill sets from multiple engineering disciplines from software and hardware in particular Building Embedded Systems is a book about helping you do things in the right way from the beginning of your first project Programmers who know software will learn what they need to know about hardware Engineers with hardware knowledge likewise will learn about the software side Whatever your background is Building Embedded Systems is the perfect book to fill in any knowledge gaps and get you started in a career programming for everyday devices Author Changyi Gu brings more than fifteen years of experience in working his way up the ladder in the field of embedded systems He brings knowledge of numerous approaches to embedded systems design including the System on Programmable Chips SOPC approach that is currently growing to dominate the field His knowledge and experience make Building Embedded Systems an excellent book for anyone wanting to enter the field or even just to do some embedded programming as a side project What You Will Learn Program embedded systems at the hardware level Learn current industry practices in firmware development Develop practical knowledge of embedded hardware options Create tight integration between software and hardware Practice a work flow leading to successful outcomes Build from transistor level to the system level Make sound choices between performance and cost Who This Book Is For Embedded system engineers and intermediate electronics enthusiasts who are seeking tighter integration between software and hardware Those who favor the System on a Programmable Chip SOPC approach will in particular benefit from this book Students in both Electrical Engineering and Computer Science can also benefit from this book and the real life industry practice it provides

**Implementation of a CAN Bus based Measurement System on an FPGA Development Kit** Yu Zhu, 2010-07-05 Inhaltsangabe Abstract The Controller Area Network CAN is a serial field bus protocol which was originally used in road vehicles Most people still use Microcontrollers MCU to control the CAN bus The development of Field programmable Gate Array FPGA is very advanced and compared to the MCU the FPGA has many advantages For this reason this thesis uses an Altera FPGA development kit to design a CAN bus based measurement system During the work four Direct Digital Synthesizers DDS were simulated for four wave channels in the FPGA All signals of the channels were transmitted from the FPGA to a CAN bus Between the CAN bus and FPGA an Atmel CAN MCU which contains both serial and CAN ports was used as the third party Whereby the data output from the FPGA were first transmitted to the serial port of the CAN MCU and then shifted to the CAN port of the CAN MCU The CAN bus device NI CAN USB 8473 which

was used in this thesis has a CAN port to connect to the CAN port of the CAN MCU and a USB 2.0 port to connect to a PC. Finally, the data of the CAN bus was monitored on the PC with the LabVIEW platform. With this platform, the data could also be transmitted to the CAN bus and then to the FPGA to change the tuning word of DDS. In order to achieve the speed limit of the complete measurement system, the communication rates of FPGA, CAN bus, and CAN MCU were programmed to be the maximum.

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*Embedded SoPC Design with Nios II Processor and VHDL Examples* Pong P. Chu, 2011-08-29 The book is divided into four major parts. Part I covers HDL constructs and synthesis of basic digital circuits. Part II provides an overview of embedded software development with the emphasis on low-level I/O access and drivers. Part III demonstrates the design and development of hardware and software for several complex I/O peripherals including PS2 keyboard and mouse, a graphic video controller, an audio codec, and an SD secure digital card. Part IV provides three case studies of the integration of hardware accelerators including a custom GCD (greatest common divisor) circuit, a Mandelbrot set fractal circuit, and an audio synthesizer based on DDFS (direct digital frequency synthesis) methodology. The book utilizes FPGA devices, Nios II soft core processor, and development platform from Altera Co, which is one of the two main FPGA manufacturers. Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions; details at [www.altera.com/university](http://www.altera.com/university). The two main educational prototyping boards are known as DE1-99 and DE2-69. All experiments can be implemented and tested with these boards. A board combined with this book becomes a turn-key solution for the SoPC design experiments and projects. Most HDL and C codes in the book are device-independent and can be adapted by other prototyping boards as long as a board has similar I/O configuration.

**EDN**, 2009 [Selected Papers of the International Workshop on Smalltalk Technologies](#) Michael Haupt, 2010 The goal of the IWST workshop series is to create and foster a forum around advancements of or experience in Smalltalk. The workshop welcomes contributions to all aspects, theoretical as well as practical, of Smalltalk-related topics.

**A Hacker's Guide to Project Management** Andrew K. Johnston, 1995 This practical guide for all project managers is packed with examples, hints, and tips. It will enable managers to manage and build

better systems on time and within budget The book contains many Q A sessions which indicate common questions and answers found by experienced project managers

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A User's Guide to GALAXY 3 V. J. Bell,United Kingdom Atomic Energy Authority,United Kingdom Atomic Energy Authority. Reactor Group,Atomic Energy Establishment. General Reactor Physics Division,L. W. Blott,W. M. M. Kerr,K. Parker,I. C. Pull,R. D. Wade,D. V. J. Williams,1964 **FITPACK User Guide** P. Dierckx,1987 BASIC User's Guide ,1984

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