

3Answer any **four** parts :**5×4=20**

- (a) Sketch the memory read Bus Cycle timings for 8086. Will there be any difference in case of 8088?
- (b) From pin description, it seems that 8086 has only 16 AD lines i.e. AD0-AD15. The physical address space however, is larger than 2^{16} . How this contradiction is handled? Explain with a neat diagram.
- (c) Can 8087 be used as an independent processor? Compared to 8086, does 8087 have a separate (i) address space, (ii) instruction set? Explain.
- (d) Write a short note on 8284A. Is the RESET in 8086 synchronous or asynchronous?
- (e) Explain the floating point number formats used in a PC with 8087. What are the advantages and disadvantages of using floating point formats?
- (f) Write a short note on 8086 status pins in Maximum Mode.

4Answer any **two** parts :**10×2=20**

- (a) What is interrupt driven data transfer? Is it a form of programmed data transfer or not? While writing an interrupt service subroutine, what special features are usually incorporated extra in comparison to a normal subroutine? Justify. What is the difference in execution of RET and IRET instructions?
- (b) Give the Bus Cycle timing diagrams for IN and OUT instructions. How are they different from comparable MOV instructions? Can MOV instruction be ever used for I/O operation? If yes, what the I/O type is then called? If no, why not?

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1. Understanding the eBook Microprocessor And Its Application Question Paper
 - The Rise of Digital Reading Microprocessor And Its Application Question Paper
 - Advantages of eBooks Over Traditional Books
2. Identifying Microprocessor And Its Application Question Paper
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Microprocessor And Its Application Question Paper
 - User-Friendly Interface
4. Exploring eBook Recommendations from Microprocessor And Its Application Question Paper
 - Personalized Recommendations
 - Microprocessor And Its Application Question Paper User Reviews and Ratings

- Microprocessor And Its Application Question Paper and Bestseller Lists
- 5. Accessing Microprocessor And Its Application Question Paper Free and Paid eBooks
 - Microprocessor And Its Application Question Paper Public Domain eBooks
 - Microprocessor And Its Application Question Paper eBook Subscription Services
 - Microprocessor And Its Application Question Paper Budget-Friendly Options
- 6. Navigating Microprocessor And Its Application Question Paper eBook Formats
 - ePub, PDF, MOBI, and More
 - Microprocessor And Its Application Question Paper Compatibility with Devices
 - Microprocessor And Its Application Question Paper Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Microprocessor And Its Application Question Paper
 - Highlighting and Note-Taking Microprocessor And Its Application Question Paper
 - Interactive Elements Microprocessor And Its Application Question Paper
- 8. Staying Engaged with Microprocessor And Its Application Question Paper
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Microprocessor And Its Application Question Paper
- 9. Balancing eBooks and Physical Books Microprocessor And Its Application Question Paper
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Microprocessor And Its Application Question Paper
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Microprocessor And Its Application Question Paper
 - Setting Reading Goals Microprocessor And Its Application Question Paper
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Microprocessor And Its Application Question Paper
 - Fact-Checking eBook Content of Microprocessor And Its Application Question Paper
 - Distinguishing Credible Sources

13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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