

Code No: 118BJ

R13

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

B. Tech IV Year II Semester Examinations, September - 2020

EMBEDDED SYSTEMS

(Computer Science and Engineering)

Time: 2 hours

Max. Marks: 75

Answer any Five Questions

All Questions Carry Equal Marks

- 1.a) Briefly describe the distinction between requirements and specification. [7+8]
b) Explain the formalisms for system design.
- 2.a) Explain the memory organization of 8051 in detail. [5+10]
b) Explain the operation of counters and timers in 8051.
- 3.a) Explain the assembly language programming process. [5+10]
b) Explain the Data Transfer Instructions in 8051 with examples.
- 4.a) What are the different logical instructions available in 8051? Write their syntax. [7+8]
b) Write an assembly language program to 8-bit addition and subtraction.
- 5.a) Explain about serial data communication modes in 8051. [7+8]
b) Draw and explain the A/D and D/A converters.
- 6.a) Explain task scheduling mechanisms with an example. [8+7]
b) Explain how interrupt routines handled in RTOS Environment.
- 7.a) Discuss Software development tools of embedded system. [7+8]
b) Write short notes on the following
i) Testing on host machine
ii) Laboratory tools.
- 8.a) Discuss briefly about SHARC architecture. [9+6]
b) Write about Internet enabled systems.

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Operating System Question Bank with Answers: A Comprehensive Handbook S. R. Jena, Suman Kumar, 2024-11-05 The dynamic field of computer science is ever evolving and with it the need for comprehensive and structured learning materials becomes increasingly essential As educators deeply engaged in nurturing the academic growth of our students at NIMS University Jaipur Rajasthan we identified the necessity for a specialized resource that not only aids learners in understanding core concepts but also challenges them to think critically apply their knowledge and analyze complex problems This recognition inspired us to create *Operating System Question Bank with Answers A Comprehensive Handbook* This handbook is meticulously designed to align with Bloom's Taxonomy a framework that emphasizes the importance of higher order thinking skills By structuring our questions and answers according to Bloom's hierarchy we aim to provide a balanced approach that covers everything from basic recall and understanding to more complex tasks such as analysis evaluation and synthesis This structure ensures that students develop a deeper understanding of Operating Systems and are better prepared for academic evaluations competitive exams and professional applications The content in this handbook has been carefully curated and refined through our extensive experience in teaching the Operating Systems subject at NIMS University Each question has been selected and crafted to reflect key concepts and applications relevant to the field accompanied by detailed well explained answers This format not only aids in self assessment but also serves as a strong guide for instructors and students alike We believe this handbook will prove to be an invaluable resource for students educators and professionals looking to reinforce their knowledge of Operating Systems It is our hope that through this work learners will find a supportive tool that enriches their educational journey stimulates their critical thinking and deepens their understanding of one of the foundational subjects in computer science We express our sincere gratitude to NIMS University for providing an environment that fosters learning and teaching excellence It is our students enthusiasm and the academic spirit of the university that motivated us to compile this question bank We hope this contribution aids many in achieving their academic and professional goals

Digital Systems & Microprocessors: Objective Question Bank for North American Graduates Chandresh Agrawal, Nandini Books, 2026-03-06 Ebook Details Primary Title Digital Systems Study Aids Professional Licensure Ebook Description Master the Core of Computer Engineering through Focused Practice For engineering graduates in the USA and Canada a deep understanding of digital logic and microprocessor fundamentals is essential for both the FE Exam and professional technical interviews Digital Systems Microprocessors provides a high volume question bank designed to reinforce these critical concepts What is Covered This volume focuses on the hardware and low level software interface Digital Logic Design Logic gates Boolean minimization K maps and combinational circuit analysis Microprocessor Fundamentals Extensive practice on the 8085 architecture which serves as the academic standard for learning instruction sets and timing Memory I O Interfacing Questions on addressing modes memory mapping and peripheral interfacing

Interrupts Control Detailed sets on interrupt structures stack operations and timing diagrams What is NOT Covered High Level Programming This book does not cover C Python or Java It is strictly focused on assembly level architecture and hardware Advanced VHDL Verilog While logic design is covered hardware description languages are not the focus Step by Step Explanations To provide a high volume of questions at an affordable price this book provides the Answer Key but does not include lengthy theoretical derivations The Instant Book Concept Order Download Print We believe that complex logic diagrams and timing charts are best studied on paper This ebook is optimized for our Instant Book Concept Immediate Download Purchase and receive your 550 page PDF instantly No waiting for 7 day shipping within the US or Canada Professional Print Ready Formatted for high quality printing on standard Letter 8 5 x 11 or A4 paper The Spiral Bound Advantage We strongly recommend printing the file and having it spiral bound at a local shop like FedEx Office or Staples A spiral bound book lies flat on your desk allowing you to trace logic paths and solve Boolean equations directly on the page a far superior experience to a digital screen Modular Revision Print individual practice sets to take to your study group or library keeping your preparation organized and portable Trademark Content Disclaimer Disclaimer to Candidates NCEES and FE are registered trademarks of the National Council of Examiners for Engineering and Surveying This publication is an independent study resource authored by Dr Chandresh Agrawal and is not affiliated with endorsed by or sponsored by NCEES The questions provided are original practice sets designed to align with graduate level engineering standards and are not reproduced from actual past exam papers provided by the council

CBSE Class 12 - Electronics and Hardware Question Bank Mocktime Publication,101-01-01 CBSE 12 Question Bank Electronics and Hardware **UGC NET Paper 2 Electronics Science Chapterwise Question Bank | Solved Questions with Detailed Explanations | Based on Updated Exam Pattern** Nodia,2026-03-09 Prepare effectively for the UGC NET Electronics Science examination with this comprehensive Chapterwise MCQ Bank designed to help aspirants strengthen their conceptual understanding and improve exam performance The book includes a wide range of carefully selected multiple choice questions organized chapter wise allowing students to practice each topic systematically Every question is accompanied by clear and detailed explanations enabling readers to understand the reasoning behind the correct answers and build a strong foundation in electronics concepts This book strictly follows the latest UGC NET syllabus and updated exam pattern making it a reliable and practical preparation guide The MCQs are designed to match the actual difficulty level and structure of the UGC NET exam helping candidates become familiar with the types of questions asked in the examination With additional important and predicted questions for upcoming exams this book serves as a valuable resource for revision practice and self assessment for all serious UGC NET Electronics Science aspirants

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