

|                      |                  |                  |                |
|----------------------|------------------|------------------|----------------|
| ACALL addr11         | DIV AB           | LJMP addr16      | RETI           |
| ADD A,<src           | DJNZ <byte,<rel8 | MOV <dest,<src   | RL A           |
| ADDC A,<src          | INC <byte        | MOV DPTR,#data16 | RLC A          |
| AJMP addr11          | INC DPTR         | MOV bit,bit      | RR A           |
| ANL <dest,<src       | JB bit,rel8      | MOVC A,@A+<base  | RRC A          |
| ANL <bit             | JBC bit,rel8     | MOVX <dest,<src  | SETB bit       |
| CJNE <dest,<src,rel8 | JC rel8          | MUL AB           | SJMP rel8      |
| CLR A                | JMP @A+DPTR      | NOP              | SUBB A,<src    |
| CLR bit              | JNB bit,rel8     | ORL <dest,<src   | SWAP A         |
| CPL A                | JNC rel8         | ORL C,bit        | XCH A,<byte    |
| CPL bit              | JNZ rel8         | POP direct       | XCHD A,@Ri     |
| DA A                 | JZ rel8          | PUSH direct      | XRL <dest,<src |
| DEC <byte            | LCALL addr16     | RET              |                |

# Instructions Set Of 8051 Microcontroller

**Daniela Niemeyer**



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