

**Model Question Paper-II with effect from 2021 (CBCS Scheme)**

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**First/Second Semester B.E Degree Examination**  
**Basic Electronics & Communication Engineering**

**Max. Marks: 100**

**Note:** 01. Answer any FIVE full questions, choosing at least ONE question from each MODULE.

<b>Module -1 (Power Supplies, Amplifiers, Operational amplifiers, Oscillators)</b>			<b>Marks</b>
Q.01	a	Explain the working of Bi-phase Full wave rectifier circuit with neat diagram.	8
	b	List and describe the main types of amplifiers.	7
	c	Describe the working of a single stage astable oscillator using an opamp.	5
OR			
Q.02	a	Explain the operation of a simple shunt Zener voltage regulator.	7
	b	Sketch the circuit of each of the following based on the use of operational amplifiers (a) comparator (b) a differentiator (c) an integrator (d) Inverting Amplifier.	8
	c	With circuit diagram explain the following: Voltage Doubler, Voltage Tripler	5
<b>Module-2 (Logic Circuits, Data representation, Shift registers, Counters)</b>			
Q. 03	a	Design a 3-to-8 Decoder and show its implementation using basic gates.	8
	b	Construct a logic circuit that will produce a Logic 1 output whenever two or more of its inputs are at Logic 1.	7
	c	With the help of truth table explain full adder using logic gates.	5
OR			
Q.04	a	Explain Input and output states for a J-K bistable using clocked operation.	8
	b	With the help of a neat diagram explain the 4-bit shift register operation and types.	7
	c	With a neat block diagram explain the arrangement of a micro-controller system with typical inputs and outputs.	5
<b>Module-3 (Embedded Systems, Sensors and Interfacing, Actuators, Communication Interface)</b>			
Q. 05	a	Compare Embedded systems and general computing systems. Also provide major application areas of Embedded Systems.	8
	b	Explain the different configurations of 7-segment LED Display.	6
	c	Describe the matrix keyboard interfacing and UART.	6
OR			

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**Robert R. Redfield**



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