Prabhat Mishra and Nikil D. Dutt

FUNCTIONAL VERIFICATION OF PROGRAMMABLE EMBEDDED ARCHITECTURES
A Top-Down Approach

Springer

Functional Verification Of Programmable Embedded Architectures A Top Down Approach

Institute of Electrical and Electronics Engineers

Functional Verification Of Programmable Embedded Architectures A Top Down Approach:

   Functional Verification of Programmable Embedded Architectures Prabhat Mishra,Nikil D. Dutt,2005-07 Validation of programmable architectures consisting of processor cores coprocessors and memory subsystems is one of the major bottlenecks in current System on Chip design methodology A critical challenge in validation of such systems is the lack of a golden reference model As a result many existing validation techniques employ a bottom up approach to design verification where the functionality of an existing architecture is in essence reverse engineered from its implementation Traditional validation techniques employ different reference models depending on the abstraction level and verification task resulting in potential inconsistencies between multiple reference models This book presents a top down validation methodology that complements the existing bottom up approaches It leverages the system architect s knowledge about the behavior of the design through architecture specification using an Architecture Description Language ADL The authors also address two fundamental challenges in functional verification lack of a golden reference model and lack of a comprehensive functional coverage metric Functional Verification of Programmable Embedded Architectures A Top Down Approach is designed for students researchers CAD tool developers designers and managers interested in the development of tools techniques and methodologies for system level design microprocessor validation design space exploration and functional verification of embedded systems   ***Customizable Embedded Processors*** Paolo Ienne,Rainer Leupers,2006-08-30 Customizable processors have been described as the next natural step in the evolution of the microprocessor business a step in the life of a new technology where top performance alone is no longer sufficient to guarantee market success Other factors become fundamental such as time to market convenience energy efficiency and ease of customization This book is the first to explore comprehensively one of the most fundamental trends which emerged in the last decade to treat processors not as rigid fixed entities which designers include as is in their products but rather to build sound methodologies to tailor fit processors to the specific needs of such products This book addresses the goal of maintaining a very large family of processors with a wide range of features at a cost comparable to that of maintaining a single processor First book to present comprehensively the major ASIP design methodologies and tools without any particular bias Written by most of the pioneers and top international experts of this young domain Unique mix of management perspective technical detail research outlook and practical implementation   ***System-Level Validation*** Mingsong Chen,Xiaoke Qin,Heon-Mo Koo,Prabhat Mishra,2012-09-25 This book covers state of the art techniques for high level modeling and validation of complex hardware software systems including those with multicore architectures Readers will learn to avoid time consuming and error prone validation from the comprehensive coverage of system level validation including high level modeling of designs and faults automated generation of directed tests and efficient validation methodology using directed tests and assertions The methodologies described in this book will help designers to improve the quality of their validation performing as much

validation as possible in the early stages of the design while reducing the overall validation effort and cost **Processor Description Languages** Prabhat Mishra,Nikil Dutt,2011-07-28 Efficient design of embedded processors plays a critical role in embedded systems design Processor description languages and their associated specification exploration and rapid prototyping methodologies are used to find the best possible design for a given set of applications under various design constraints such as area power and performance This book is the first comprehensive survey of modern architecture description languages and will be an invaluable reference for embedded system architects designers developers and validation engineers Readers will see that the use of particular architecture description languages will lead to productivity gains in designing particular application specific types of embedded processors Comprehensive coverage of all modern architecture description languages use the right ADL to design your processor to fit your application Most up to date information available about each architecture description language from the developers save time chasing down reliable documentation Describes how each architecture desccription language enables key design automation tasks such as simulation synthesis and testing fit the ADL to your design cycle American Book Publishing Record ,2003 *The British National Bibliography* Arthur James Wells,2005 **Functional Verification Of Programmable Embedded Architectures** Mishra Prabhat Et.Al,2007-12-01 *Dissertation Abstracts International* ,2004 **Language-driven Exploration and Implementation of Partially Re-configurable ASIPs** Anupam Chattopadhyay,Rainer Leupers,Heinrich Meyr,Gerd Ascheid,2008-12-02 Increasing complexity of modern embedded systems demands system designers to ramp up their design productivity without compromising performance goals This is promoted by modern Electronic System Level ESL techniques Language driven Exploration and Implementation of Partially Re configurable ASIPs addresses an important segment of the ESL area by modeling partially re configurable processors via high level Architecture Description Language ADL This approach also hints an imminent evolution in the area of re configurable system design **Proceedings** ,2001

Proceedings VHDL International. Users Forum,1997 **Science Abstracts** ,1993 *The Engineering Index Annual* ,1992 Since its creation in 1884 Engineering Index has covered virtually every major engineering innovation from around the world It serves as the historical record of virtually every major engineering innovation of the 20th century Recent content is a vital resource for current awareness new production information technological forecasting and competitive intelligence The world s most comprehensive interdisciplinary engineering database Engineering Index contains over 10 7 million records Each year over 500 000 new abstracts are added from over 5 000 scholarly journals trade magazines and conference proceedings Coverage spans over 175 engineering disciplines from over 80 countries Updated weekly **International Aerospace Abstracts** ,1984 **Index to IEEE Publications** Institute of Electrical and Electronics Engineers,1998 Issues for 1973 cover the entire IEEE technical literature Electrical & Electronics Abstracts ,1997 **CAD/CAM Abstracts** ,1989 *Government Reports Announcements & Index* ,1978 **Index to Theses with Abstracts Accepted for Higher**

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