For each of the following problems you are given an Entity-Relationship diagram. You are to convert the diagram to a Relational Schema. You may need extra sheets of paper for scratch work and drafts. Record your final relational schema on this sheet of paper.

1. Entity relationship diagram to relational schema - Exercise 1

Entity Relationship Diagram Exercise 1

Question:
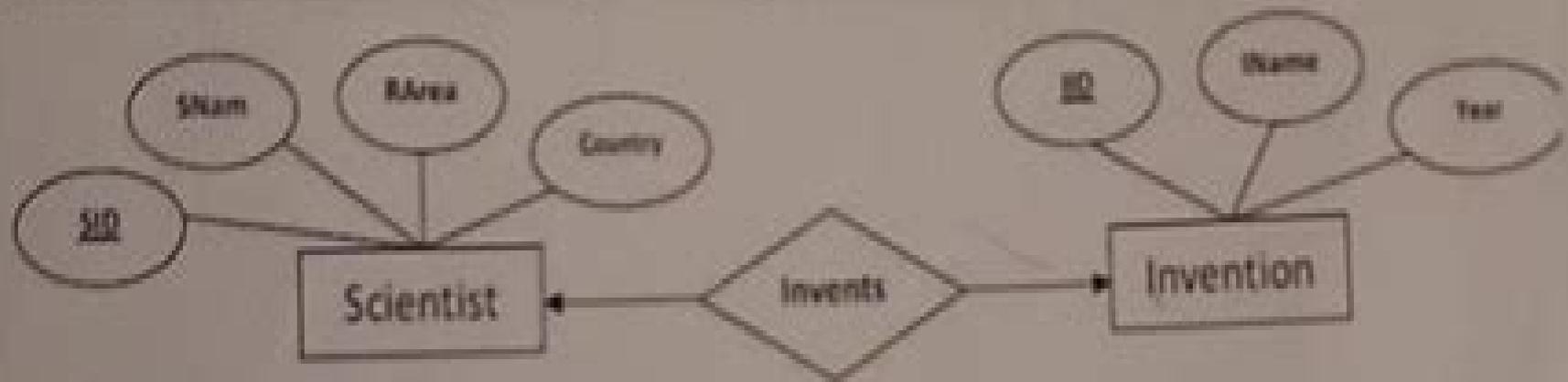Convert/reduce the ER Diagram given in figure 1 below:

Figure 1 - ER diagram with One-to-One relationship

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web gerbang logika atau logic gate adalah bagian dasar dari perancangan sistem elektronika digital untuk mengubah masukan input menjadi sinyal keluaran output yang logis sebagai hasil dari voltase atau arus gerbang logika direpresentasikan oleh angka biner atau tabel kebenaran yaitu simbol 1 dan 0

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web gerbang logika adalah penyusun elektronika digital yang setiap cara kerja rangkaian pada gerbang logika menggunakan prinsip aljabar boolean pada dasarnya dalam ilmu elektronik suatu masukan dan keluaran dibangun oleh yang namanya voltase atau arus voltase ini biasanya dihubungkan dengan sakelar

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