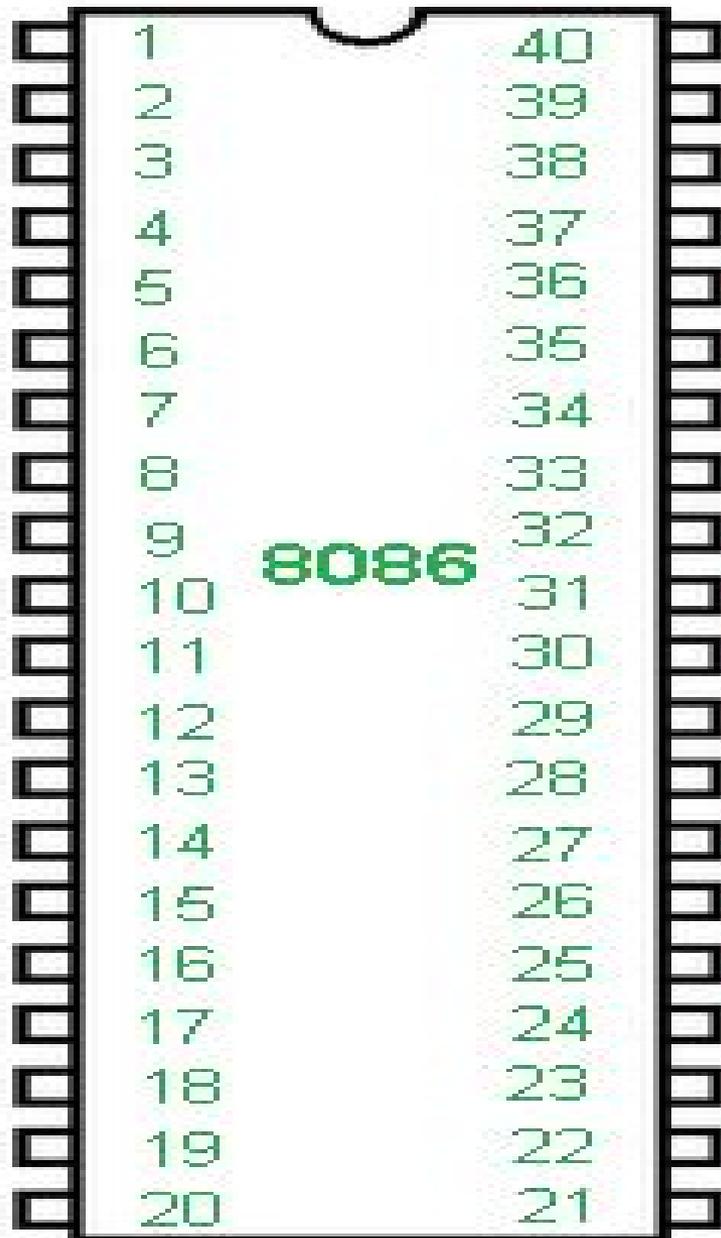


GND
AD₁₄
AD₁₃
AD₁₂
AD₁₁
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AD₉
AD₈
AD₇
AD₆
AD₅
AD₄
AD₃
AD₂
AD₁
AD₀
NMI
INTR
CLK
GND



8086

**MAX
MODE**

**MIN
MODE**

V_{CC}
AD₁₅
A₁₆/S₃
A₁₇/S₄
A₁₈/S₅
A₁₉/S₆
BHE[']/S₇
MN/MX[']
RD[']
RQ[']/GT₀[']
RQ[']/GT₁[']
LOCK[']
S₂[']
S₁[']
S₀[']
QS₀
QS₁
TEST[']
READY
RESET

HOLD
HLDA
WR[']
M/IO[']
DT/R[']
DEN[']
ALE
INTA[']

Microprocessor 8086 With Pin Diagram

Art Margolis



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