

Subject Code: XXXXX

Roll No:

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BTECH

(SEM-5) MICROPROCESSOR AND MICROCONTROLLER 2021-22

TIME:3 HOUR

Total Marks: 100

Instruction: Attempt the questions as per the given instructions. Assume missing data suitably.

SECTION - A

Attempt All Parts in Brief

2*10 = 20

Q1	Questions	Marks
(a)	Discuss the specifications of 8085 microprocessor.	2
(b)	List the disadvantages of memory mapped I/O.	2
(c)	State the number of T-states required for following instructions: MVLA, 34 H, LXI H 2000 H.	2
(d)	List all the maskable and non-maskable interrupts of 8085.	2
(e)	Explain the physical address, offset address and segment address in context to 8086.	2
(f)	Discuss the various memory segments in 8086.	2
(g)	Explain the organization of stack in 8051.	2
(h)	Describe the bit-addressable RAM space available in 8051.	2
(i)	Discuss the significance of following SFR's of 8051- PSW, TCON.	2
(j)	Describe the following instructions of 8051-(i) MOVA, @R0 (ii) MOVX A, @DPTR.	2

SECTION - B

Microprocessor And Microcontroller Question Paper 22

Victor M. Corman



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